# ATTENTION ATTENTION ATTENTION

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### NO. 7403 BEST AVAILABLE COP

Atty. Dkt. No. 039153-0667

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Qi XIANG

Title:

STRAINED SILICON MOSFET HAVING IMPROVED CARRIER MOBILITY, STRAINED SILICON

CMOS DEVICE, AND METHODS

OF THEIR FORMATION

Appl. No.:

10/684,727

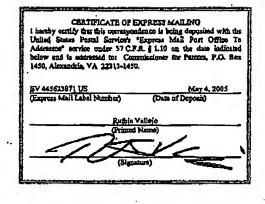
Filing Date: 10/14/2003

Examiner:

Trinh, Michael M.

Art Unit:

2822



### REQUEST FOR REFUND

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby requests a refund of \$300.00 for payment of the publication fee that was erroneously charged in the Notice of Allowance and Fee(s) Due. The above-identified application was filed with a Request and Certificate Under 35 U.S.C. 122(b)(B)(i) on October 13, 2003. The application was not published and so no publication fee was due.

Attached is a copy of check no. 881527 in the amount of \$1,700.00 for payment of the Issue Fee (\$1,400) and Publication Fee (\$300) dated December 21, 2004, and a copy of a datestamped postcard showing receipt of the check by the Patent Office. Applicant respectfully requests that a credit of \$300.00 be made to Deposit Account No. 50-0872.

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Atty. Dkt. No. 039153-0667

In view of the foregoing, it is believed that no fee is required for filing this petition, but should a fee be required, the PTO is authorized to charge an appropriate fee for the filing of this petition to Deposit Account No. 50-0872.

Respectfully submitted,

FOLEY & LARDNER LLP

Customer Number: 23392

Telephone: Facsimile:

(310) 975-7964

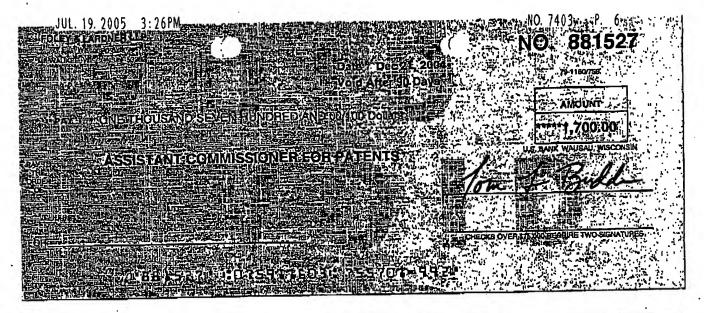
(310) 557-8475

Ronald Coslick

Attorney for Applicant

Registration No. 36,489

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Applicant: Scrial No.: Filing Date: Title:

Qi XIANG 10/684,727 10/14/2003

CARRIER

STRAINED SILICON MOSPET HAVING IMPROVE

MOBILITY, STRAINED SILICON CMOS DE METHODS OF THEIR FORMATION

**MAMMMM** 

Transmittal of Issue Fee (1 page)
Part-B Issue Fee Transmittal (PTOL-85) (1 page)
Fee Address\* Indication Form (1 page)
Transmittal for Formal Drawings (1 page)
Figures 1 and 2 (1 sheet)
Check for \$1,790.00 for payment of the Issue Fee and Publication Fee.

Date Due: Date Mailed: RCO/my 12/27/2004 12/21/2004

NO. 7403

**#FOLEY** 

FOLEY & LARDNER LLP ATTORNEYS AT LAW

2029 CENTURY PARK EAST, SUITE 3500 LOS ANGELES, CA 90067-3021 TELEPHONE: 310.277.2223 FACSIMILE: 310.557.8475 WWW.FOLEY.COM

### **FACSIMILE TRANSMISSION**

## Total # of Pages 7 (Including this page)

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Best on Charles

From: Ronald Coslick

Date: July 19, 2005

Client/Matter No: 039153-0667

User ID No: 0216

### MESSAGE:

Ro:

U.S. Patent Application No.:

10/684,727

Filing Date: Title:

STRAINED SILICON MOSFET HAVING IMPROVED CARRIER

MOBILITY, STRAINED SILICON CMOS DEVICE, AND METHODS OF

THEIR FORMATION

Inventor(s):

QI XIANG

Our Ref.:

039153-0667

Per my conversation today with your refund department, please find attached a resubmission of the request for refund filed on May 4, 2005.

I hereby certify that copies of the following paper(s) and/or fee along with any attachments referred to or identified as being attached or enclosed are being facsimile transmitted to the U.S. Patent and Trademark Office (Fax No. (571) 273-6500) on the date of shown above.

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- Copy of Request for Refund
- 3. Copy of Check # 881527 and Copy of postcard

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U.S. Express Mail No. EV 445623871 US Atty. Dkt. 039153-0667 Receipt is hereby acknowledged in the U.S. Patent and Trademark Office:

Applicant

Qi Xlang 10/684,727

Serial No.:

Filing Date: 10/14/2003

Title:

STRAINED SILICON MOSFET HAVING IMPROVED CARRIER MOBILITY, STRAINED SILICON CMOS

DEVICE, AND METHODS OF THEIR FORMATION

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